

Huffman Decoder OL_Huff

Short Form Datasheet

General Description

This core implements one clock per symbol Huffman decoding of a JPEG data stream. It is fully programmable to support user provided Huffman tables. It outputs the RUN/LENGTH code as well as the amplitude of the non zero coefficient. The design is fully synchronous.

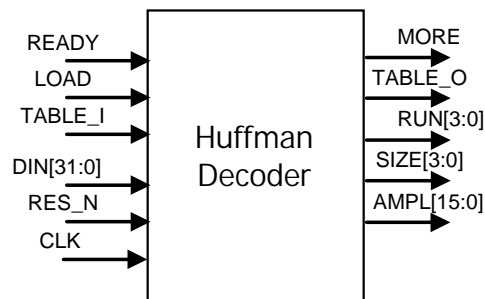
Applications

Typical applications for the Huffman Decoder include printers, desktop video editing, digital still cameras, surveillance systems, video conferencing and other consumer products.

Features

- ◆ Fully programmable Huffman decoding of JPEG stream.
- ◆ Guaranteed one clock per symbol operation.
- ◆ Multiple user provided tables (i.e. luminance and chrominance).
- ◆ High clock speed and low gate count achieved.
- ◆ Silicon proven.
- ◆ Low licensing cost : US \$ 10,000 for single use source.
- ◆ Suitable for JPEG implementations.
- ◆ Fully synchronous design.
- ◆ Available as fully functional and synthesizable VHDL or Verilog core.
- ◆ Testbench provided.

Symbol



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